

EXPRESSION OF DEPLETION REGION

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When a metal and n-type semiconductor are brought into close contact to form a metal semiconductor junction, the Fermi level of the two materials coincide at thermal equilibrium by removal of electrons from semiconductor side to metal at the interface.

As a result a barrier potential is created which further opposes the movement of electron from semiconductor to metal. This ~~potential~~ potential barrier is also called depletion region or the space charge region.

If $\rho(x)$ be the volume density of space charge and ϵ is the permittivity of the semiconductor.

Then, According to differential form of Poisson's equation we have

$$\frac{dE}{dx} = \frac{\rho(x)}{\epsilon} \quad \text{--- (1)}$$

where

E is the electric field in the region

$\epsilon =$ permittivity of medium

At $x=0$

electric field $E=0$

$$\int_0^x dE = \int_0^x \frac{\rho(x)}{\epsilon} dx$$

$$\begin{aligned} [E]_0^x &= \frac{eN_D}{\epsilon} \int_0^x dx & [0, \rho(x) = eN_D] \\ &= \frac{eN_D}{\epsilon} [x]_0^x \\ &= \frac{eN_D}{\epsilon} [x]_0^x \end{aligned}$$

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$$\text{or } E = \frac{-eN_D}{\epsilon} (\omega - x) \quad \text{--- (1)}$$

From equation (1) we can observe that
The electric field is maximum at $x=0$

\therefore The maximum barrier field is given by

$$|E| = \frac{eN_D \omega}{\epsilon} \text{ at } x=0$$

Now, the electric potential gradient at any point inside the semiconductor is

$$E = \frac{-dv}{dx} = \frac{-eN_D}{\epsilon} (\omega - x)$$

$$\text{or } \frac{dv}{dx} = \frac{eN_D}{\epsilon} (\omega - x)$$

At $x=0$
 $v=0$

$$\int_0^v dv = \frac{eN_D}{\epsilon} \int_0^x (\omega - x) dx$$

$$\text{or } [v-0] = \frac{eN_D}{\epsilon} \left(\omega x - \frac{x^2}{2} \right)$$

$$\text{or } v = \frac{eN_D}{\epsilon} \left(\omega x - \frac{x^2}{2} \right)$$

At $x=\omega$ $v = V_B$

we get

$$V_B = \frac{eN_D}{\epsilon} \left(\omega^2 - \frac{\omega^2}{2} \right)$$

$$\therefore V_0 = \frac{eN_D}{\epsilon} \left[\frac{\omega^2}{2} - \frac{\omega^2}{2} \right]$$

$$V_0 = \frac{eN_D}{\epsilon} \left[\frac{\omega^2}{2} \right]$$

$$\text{or } \omega = \sqrt{\frac{2V_0 \epsilon}{eN_D}}$$

Thus, the depletion width ω and potential barrier V_0 is given by

$$\omega = \sqrt{\frac{2V_0 \epsilon}{eN_D}}$$

Imp points:

When no bias voltage is applied then, $V_0 = V_{bi}$

For forward bias voltage V_F we substitute $V_0 = (V_{bi} - V_F)$

For reverse bias voltage (V_R) we substitute $V_0 = (V_{bi} + V_R)$ respectively.

So, it is clear that the width of depletion region increases or decreases with forward or reverse biasing applied voltage.

Schottky Diode I-V Characteristics

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The I-V characteristics of Schottky diode is shown here. In forward bias, the current rises exponentially, having a knee or turn on voltage of around 0.2V.

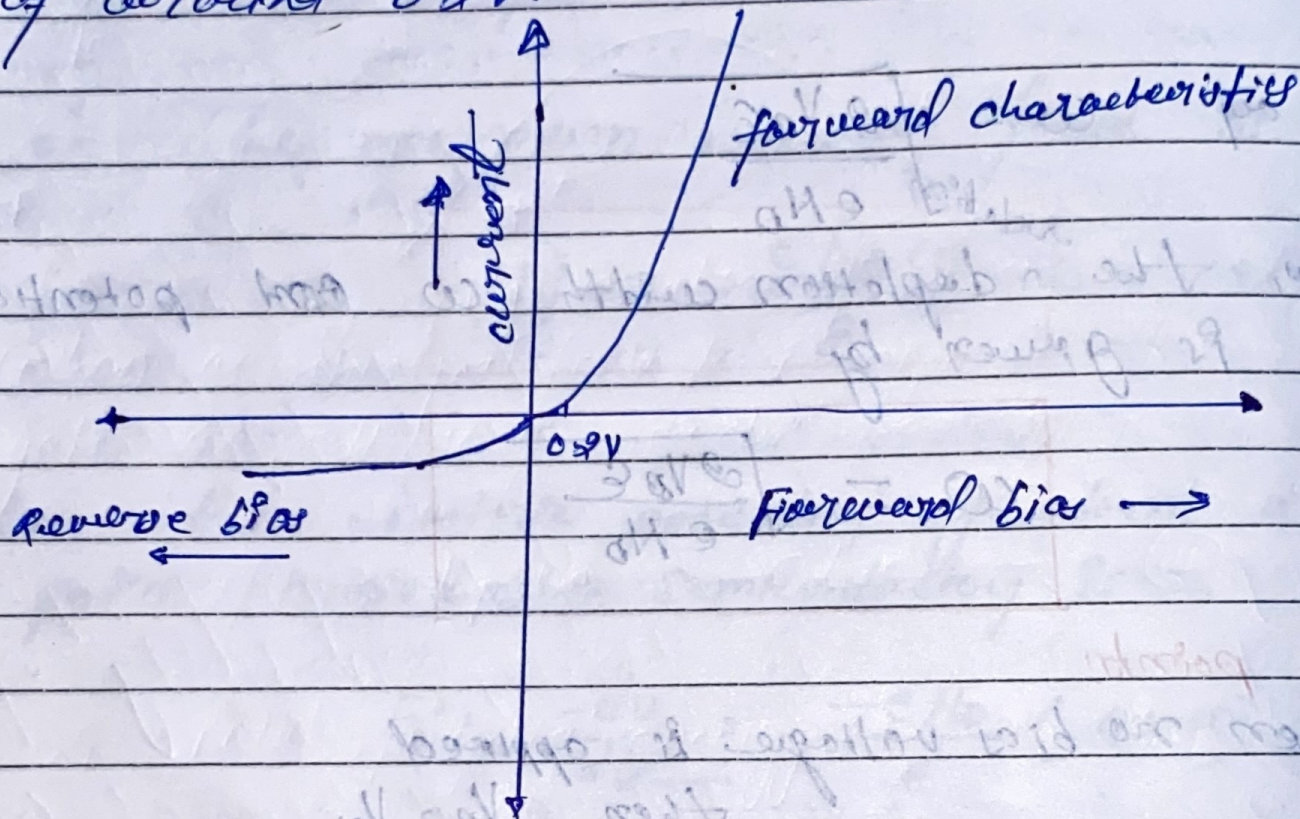


Figure I-V characteristics of Schottky diode.

In the reverse bias direction, there exist also a current in this ^{reverse} direction.

The use of a guard ring in the fabrication of diode has an effect on its performance in both forward and reverse direction.